CiteSeer Find: firmware image PCB Documents Citations

Searching for **PHRASE firmware image pcb**.
Restrict to: Header Title Order by: Citations Hubs Usage Date Try: Amazon B&N Google (RI) Google (Web) CSB DBLP
No documents match Boolean query. Trying non-Boolean relevance query.
1000 documents found. **Only retrieving 500 documents (System busy - maximum reduced).** Retrieving documents... **Order: relevance to query.**

Automation Controller Interface Specification - Hewlett Packard Version (Correct)
CMD_DATA and RDATA Length fields Reset Send **Firmware Image**, command/response sequence Ladawan Johnson
ftp.t10.org/t10/document.02/02-011r0.pdf

Evaluationof Emissionfis a PCB by Using Crosstalk between a.. - Naoto Oka Ch (2000) (Correct)
an# hoic chamber'sgroun plan was corrected byusin **image** theory. The measured resultan the calculated Compatibility Technology Evaluationof Emissionfis a **PCB** by Using Crosstalk between a Low Frequency Signal In thispi er, the evaluation of emission from a **PCB** by using crosstalk between a low frequency signal
search.ieice.org/2000/files/../pdf/e83-b_3_586.pdf

Using Model Checking to Debug Device Firmware - Kumar (2002) (Correct)
1 Using Model Checking to Debug Device **Firmware** Sanjeev Kumar #Department of Computer Science
www.cs.princeton.edu/~skumar/papers/osdi02.ps

ESP: A Language for Programmable Devices - Kumar, Mandelbaum, Yu, Li (2001) (Correct)
C file that can be used to generate e#cient **firmware** for the device and a specification that can be
www.cs.princeton.edu/~skumar/papers/pldi2001.ps

Using Model Checking to Debug Network Interface Firmware - Kumar, Li (Correct)
Using Model Checking to Debug Network Interface **Firmware** Sanjeev Kumar Department of Computer Science
www.cs.princeton.edu/~skumar/papers/usenix02Draft.pdf

PCI Bus Binding to: IEEE Std 1275-1994 Standard for Boot.. - This Foreword Is (Correct)
Standard for Boot (Initialization Configuration) **Firmware** Revision 2.1 Foreword by the Chairman of the **Firmware** Properties .18 9. ROM **Image** Format for FCode:
bananajr6000.apple.com/1275/bindings/pci/pci2_1.pdf

Unknown - (Correct)
magnetic compatibility and printed circuit board (**PCB**) constraints 1 June 1989 1. INTRODUCTION The routing of the traces on a Printed Circuit Board (**PCB**) largely effect the ElectroMagnetic Compatibility Compatibility (EMC) performance of the **PCB** with respect to both ElectroMagnetic (EM)
www-eu.semiconductors.philips.com/acrobat/applicationnotes/AN89001_ESG.pdf

Optimizing Printed Circuit Board Assembly During The Design.. - Giachetti (Correct)
the manufacturing process of printed circuit board (**PCB**) assembly. **PCB** design is a long process in which process of printed circuit board (**PCB**) assembly. **PCB** design is a long process in which many sequential assembly optimization is not performed until the **PCB** design is completed it tends to be sub-optimal
www.faim2000.isr.umd.edu/faim/export/Ta2_0006.pdf

ESG89001 Electro magnetic compatibility and printed circuit.. - Ts Ju Ne (Correct)
magnetic compatibility and printed circuit board (**PCB**) constraints 1 June 1989 1. INTRODUCTION The routing of the traces on a Printed Circuit Board (**PCB**) largely effect the ElectroMagnetic Compatibility Compatibility (EMC) performance of the **PCB** with respect to both ElectroMagnetic (EM)
www-eu3.semiconductors.com/acrobat/applicationnotes/AN89001_ESG.pdf

Depuration of PCBs in the Lake Michigan Ecosystem - Lamon, III, al. (Correct)
depuration.DRAFT -Do Not Cite 1 Depuration of **PCBs** in the Lake Michigan Ecosystem E. Conrad Lamon of physical processes controls the rate at which **PCB** concentrations change in Lake Michigan fauna. To the same water column and sediment concentrations of **PCB** in Lake Michigan, while exposure due to diet
info.envs.lsu.edu/wholeeco.pdf

Effective Search Strategies for Application-Independent.. - Joseph Dixon And (1997) (Correct)
processing also takes place.1.2 BSD's UDP and **Pcb** Implementations BSD-derived UDP implementations UDP implementations use protocol control blocks (**pcbs**) as communication end-points and manage the **pcbs** (**pcbs**) as communication end-points and manage the **pcbs** using a circular linked list store combined with a
ftp.cc.gatech.edu/pub/coc/tech_reports/1997/GIT-CC-97-02.iccn97.ps.Z

A New Dynamic Point Specification Approach To Optimise.. - Masri Ayob And (Correct)
of placement points and/or the feeder setup for the **PCB** assembly process. However, a limited number of minimising robot assembly time, feeder movements and **PCB** table movements. Experimental results show that terms of robot assembly time, feeder movements and **PCB** table movements. Keywords: Modelling,
www.cs.nott.ac.uk/~gxk/gxk1/../papers/icit02a.pdf

.2. ROM Upgrade Method - The Following Command (Correct)
7.2.1. net-flash (Reprograms the **firmware** from the network. 7.3. Configuration Variables
playground.sun.com/1275/bindings/arm/arm0_3d.pdf

PC Card Binding to IEEE Standard 1275-1994, Standard for.. - Draft Revision Date (Correct)
Revised 03/09/95 1:36 Pm Pc Card Binding Open **Firmware** Draft Document 1 Task Force Not For
If the card has an FCode program, the FCode **image** for the card is constructed in the RAM and is
playground.sun.com/1275/bindings/pccard/pccd1_2.ps

Novel Automatic PCB Inspection Technique Based on Connectivity - Mauro Hiromu Tatibana (Correct)
of the Connected Table of a Reference and a Test **Image**. The method is based on connected component
Novel Automatic **PCB** Inspection Technique Based on Connectivity MAURO
Abstract. This paper presents a novel technique for **PCB** inspection based on the comparison of the
www.visgraf.impa.br/sibgrapi97/anais/ps/art51.ps.gz

Embedded System Design - Ecen Fall Lectures (Correct)
the fundamentals of embedded system hardware and **firmware** design will be explored. Issues such as embedded techniques. Manufacturing and test engineering, **PCB** design, ground and power planes, EMI, EMC.
ece-www.colorado.edu/~mcclurel/f01syllabus.pdf

.4. Breakpoints - If The Breakpoint (Correct)
Breakpoints If the breakpoint is taken by the **firmware**, without the client program's assistance, the
bananajr6000.apple.com/1275/bindings/ppc/drafts/ppc_v110d.ps

Dynamic Memory Management for Programmable Devices - Kumar, Li (2002) (Correct)
ESP-a language for programmable devices. The **firmware** for programmable devices has to be fast and
www.cs.princeton.edu/~skumar/papers/ismm02.ps

Performance Impact of Using ESP to Implement VMMC Firmware - Kumar, Li (2002) (Correct)
Performance Impact of Using ESP to Implement VMMC **Firmware** Sanjeev Kumar, Kai Li Abstract-ESP is a
www.cs.princeton.edu/~skumar/papers/san102.ps

First 20 documents Next 20

Try your query at: Amazon Barnes & Noble Google (RI) Google (Web) CSB DBLP

CiteSeer - citeseer.org - Terms of Service - Privacy Policy - Copyright © 1997-2002 NEC Research Institute

CiteSeer Find: firmware image PCB Documents Citations

Searching for **PHRASE firmware image pcb**.
Restrict to: Header Title Order by: Citations Hubs Usage Date Try: Amazon B&N Google (RI) Google (Web) CSB DBLP
No documents match Boolean query. Trying non-Boolean relevance query.
1000 documents found. **Only retrieving 250 documents (System busy - maximum reduced).** Retrieving documents... **Order: relevance to query.**

Unknown - Processor In Idle (Correct)
Note: When a client program wants to enter the **firmware** user interface, one CPU invokes the enter client
playground.sun.com/1275/bindings/ppc/release/ppc-2_1.ps

. Other Commentary - Chrp Isa Keyboard (Correct)
in a different state when the client calls Open **Firmware**, unpredictable behavior may result if Open
www.openfirmware.com/1275/bindings/devices/postscript/8042.ps

A Comparison of Group and Minimum Setup Strategies.. - Smed, Salonen.. (2000) (Correct)
A Comparison of Group and Minimum Setup Strategies in **PCB** Assembly Jouni Smed Kari Salonen Mika Johnsson
ISSN 1239-1891 Abstract In printed circuit board (**PCB**) assembly, the majority of electronic components
and especially in printed circuit board (**PCB**) assembly. Generally speaking, the problems
www.tucs.fi/publications/techreports/TR327.ps.gz

Design of a Teleoperated Mobile Robot Test Platform - Wyard-Scott, Meng (1997) (Correct)
shall be addressed. Navigation motor control **PCB** Medium-density fibreboard Threaded rod Castor
1 Level 2 Level 3 Stepper Motor Near-Range Sensor **Pcb** Sonar **Pcb** Isa Card(s) Drive Wheel Batteries (12v)
2 Level 3 Stepper Motor Near-Range Sensor **Pcb** Sonar **Pcb** Isa Card(s) Drive Wheel Batteries (12v)
www.ee.ualberta.ca/~wyard/papers/loren/IEEEPacRim1997.ps

Fabrication of the first 150 Head Electronics units.. - Camin Cuautle Destro (1999) (Correct) (1 citation)
150 units. We have prepared the files describing the **PCB** layout and the assembly instructions and submitted
and submitted them to SMD. Electrical tests on **PCB**'s were performed by SMD srl while functional tests
of a few samples of the final version of the **PCBs**. We assembled them and checked that everything was
www.auger.org/admin/GAP_Notes/GAP1999/GAP_99_043.pdf

Optimisation for Surface Mount Placement Machines - Masri Ayob Peter (Correct)
at specific locations on a printed circuit board (**PCB**)finding an optimal robot travelling route is
technologies and heuristic methods. Optimisation in **PCB** assembly involves a list of subproblems to be
to be addressed, such as an assignment of **PCB** types to product families and to machine groups,
www.cs.nott.ac.uk/~gxk/gxk1/../papers/icit02b.pdf

Prediction of Common-Mode Currents on Cables.. - van Horck, van.. (1997) (Correct)
through cables connected to printed circuit boards (**PCBs**) are often the dominant cause interference.
to many engineers. We model such a test for a **PCB** and describe the coupling between the signal
the coupling between the signal circuits on a **PCB** and the circuits formed by the attached cables by
www.stw.nl/prorisc/workshop/proc/psz/horck.ps.gz

Efficient Demultiplexing of Incoming TCP Packets SQN TR92-01 - Paul Mckenney Ken (1993) (Correct) (16 citations)
made by looking up a protocol control block (**PCB**) for the segment, based on information in the
based on information in the segment's header. **PCB** lookup (a form of demultiplexing) is typically one
en route to the same application (i.e. uses the same **PCB**) as the previous TCP segment. In these
www.rdrop.com/users/paulmck/tcpdemux.ps.Z

Observations on PCB Assembly Optimization - Johnsson, Smed (2001) (Correct)
Observations On **Pcb** Assembly Optimization Mika Johnsson Valor
problems encountered in printed circuit board (**PCB**) assembly. The classification is based on the
in the literature. 1. Introduction The problems in **PCB** assembly vary from deciding the component
staff.cs.utu.fi/~jounsmed/papers/apex2001.pdf

A fast modular RLE-based inspection scheme for PCBs - Ercal Bunyak Hao (1997) (Correct)
requires acquisition and processing of gigabytes of **image** data in a matter of few seconds, especially when
www.umr.edu/~ercal/pubs/isam97.ps

Development of an Integrated CAD Tool for Switching Power.. - Wu, Tse, Chan (Correct)
approach and develops the printed circuit board (**PCB**) layout with emphasis on electromagnetic
model assignments of EMI sources, components, and **PCB** tracks. An off-line forward converter is built and
as an indicator for the EMC performance of a given **PCB** layout. Index Terms-Computer-aided design
www.en.polyu.edu.hk/~cktse/pdf-paper/IA-9803.pdf

. User Interface Commands 9.1. Open Firmware-defined.. - None Device-Specific.. (Correct)
the drive. 9. User Interface Commands 9.1. Open **Firmware**-defined User Interface Commands None. 9.2.
bananajr6000.apple.com/1275/bindings/devices/postscript/fdc.ps

.1. Display devices - Display Device Packages (1992) (Correct) (1 citation)
PowerPC Reference Platform binding to Open **Firmware** August 5, 1996 Revision 0.03 DRAFT 15 of 15 1
contains the value 20. This indicates that the ELF **image** is for the PowerPC instruction set. 7.1.2.2.
www.openfirmware.com/1275/bindings/prep/prepd0_3.ps

Production planning problems in printed circuit board.. - Crama, Klundert, Spieksma (2000) (Correct)
(80,35)40 j 0 6 pick station j 1 j 2 **image** process station j 3 -component eject j 4
and gures The assembly of printed circuit boards (**PCBs**) has generated a huge amount of industrial
of industrial activity over the last 20 years. **PCBs** are consumed as inputs by three major industrial
www.cwi.nl/donet/PSFILES/SurvDO99.ps

Context-Sensitive Filtering in RLE for PCB Inspection - Ercal, Bunyak, Feng (Correct)
methodology based on the segmentation of the **PCB image** into basic patterns and context sensitive
Context-Sensitive Filtering in RLE for **PCB** Inspection Fikret Ercal, Filiz Bunyak, Hao Feng
This paper presents a fast printed circuit board (**PCB**) inspection methodology based on the segmentation
www.umr.edu/~ercal/pubs/sigspie98.ps

Documents 21 to 40 Previous 20 Next 20

Try your query at: Amazon Barnes & Noble Google (RI) Google (Web) CSB DBLP

CiteSeer - citeseer.org - Terms of Service - Privacy Policy - Copyright © 1997-2002 NEC Research Institute

CiteSeer Find: firmware and bootstrap Documents Citations

Searching for **firmware and bootstrap**.
Restrict to: Header Title Order by: Citations Hubs Usage Date Try: Amazon B&N Google (RI) Google (Web) CSB DBLP
7 documents found. **Order: citations weighted by year.**

A Secure Active Network Environment Architecture - Alexander (1998) (Correct) (16 citations)
the operating system, whether hardware, **firmware**, or both, were trusted. We find it surprising, Ideally, the signature would be embedded in the **firmware** of the ROM. Expansion ROMs System BIOS Initiate in a trusted state with the AEGIS secure **bootstrap** architecture. We guarantee that the system www.cis.upenn.edu/~waa/sane.ps

A Secure Active Network Environment Architecture - .. - Alexander.. (1998) (Correct) (16 citations)
the operating system, whether hardware, **firmware**, or both, were trusted. We find it surprising, components of the architecture. Initialization **Firmware** Loadable Modules Module Checking Caml in a trusted state with the AEGIS secure **bootstrap** architecture. We guarantee that the system www.cis.upenn.edu/~salex/ps/sane.ps.gz

A Secure and Reliable Bootstrap Architecture - Arbaugh, Farber, Smith (1997) (Correct) (6 citations)
the operating system, whether hardware, **firmware**, or both, are trusted. We find it surprising, card. He does not address the verification of any **firmware** (system BIOS or expansion cards)Clark's model, A Secure and Reliable **Bootstrap** Architecture y William A. Arbaugh z David J. www.cis.upenn.edu/~switchware/papers/aegis.ps

Porting NetBSD to JavaStation-NC - Ushakov (Correct)
code from NetBSD's sparc64 port as possible. 3. **Firmware** and boot loader The first thing that was needed boot loader. Like Ultra machines Krups uses Open **Firmware** (OFW)Fortunately, NetBSD/sparc port already was relocated to a lower address and memory **bootstrap** code was tweaked to start its heap past OFW. 2002.eurobsdcon.org/papers/ushakov.ps

Overview of Spontaneous Networking - Evolving Concepts and.. - Preuss, Cap (1999) (Correct)
automatically by media access controllers (MAC)**firmware**, or device drivers of the operating system, or applicable for dynamic environments. BOOTP The **Bootstrap** Protocol has been designed for booting 1999. CG85] Bill Croft and John Gilmore. **Bootstrap** Protocol (BOOTP)IETF Internet Draft, RFC 951, wwwtec.informatik.uni-rostock.de/~spr/SpoNet/articles/EvolSpoNet.pdf

Experience Developing an Object-Oriented Parallel Operating.. - Schröder-Preikschat (1995) (Correct)
VLSI chip design and the development of hardware, **firmware**, and software (i.e.operating system, runtime Thus, this approach is the key to overcoming the **bootstrap** problem discussed earlier. 2.2 Object it stores a pointer to the points object used to **bootstrap** a thread instance. The thread constructors ftp.gmd.de/gmd/peace/ibm95.ps.gz

Index–3 - Executable Files (Correct)
Board Software Developer's Kit. See EBSDK. **firmware** Software or a set of instructions that is in PALmode .Loads and executes the next level of **firmware** 4-2 PALcode and the Evaluation Board 4 3-9 Associated literature, A-2 Atomic, 1-4 B **Bootstrap** process, 4-5 C CALL_PAL Entry points ftp.fwi.uva.nl/pub/comp/NetBSD/misc/dec-docs/ec-qfglb-te.ps.gz

Try your query at: Amazon Barnes & Noble Google (RI) Google (Web) CSB DBLP

CiteSeer - citeseer.org - Terms of Service - Privacy Policy - Copyright © 1997-2002 NEC Research Institute